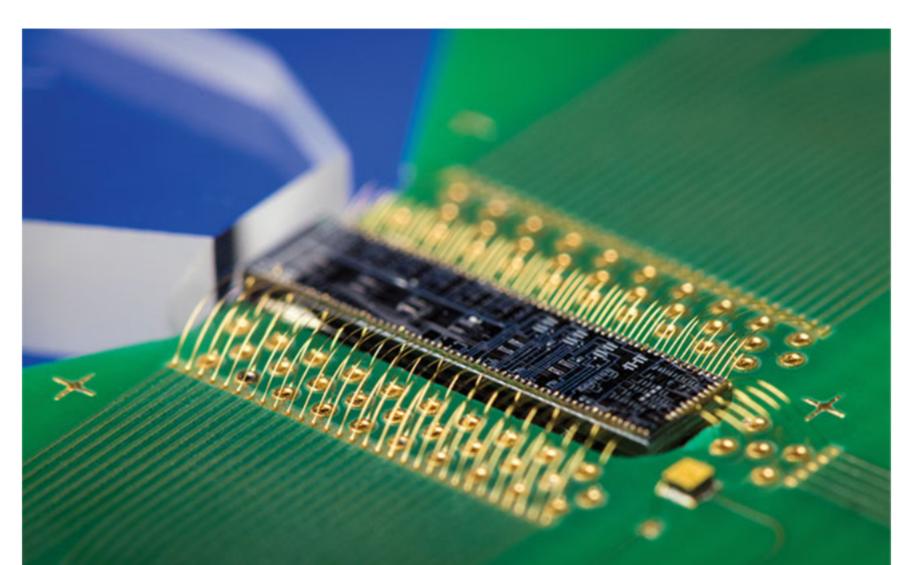


## Integrated Photonics Newsletter

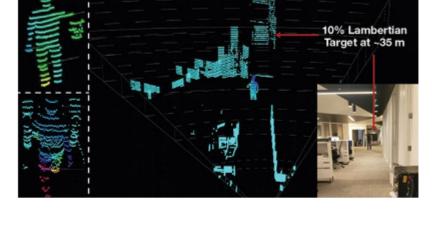




## Five Common Design Mistakes Can Send PIC Packaging Costs Skyrocketing

We have all heard that photonic integrated circuits (PICs) can pack a ton of functionality into high-performance, energyefficient, and low-cost microchips, as well as the promise that they will enable a revolution of powerful optoelectronic devices for a host of applications. However, the complexity of transforming chips into functional devices is often underestimated and the cost of PIC packaging can take a notoriously big chunk out of the development budget. Read Article





# Power Use to Expand Adoption Chip-based lidar is poised to revolutionize an already fast-

On-Chip Lidar Sheds Cost, Size, and

emerging technology sector. Unlike conventional systems, chip-based lidar can be fabricated using standard semiconductor manufacturing techniques, allowing for significant cost reduction, smaller form factors, and more scalable manufacture. Read Article





More News

Quantum-Dot Laser Elevates Efficacy of Frequency Combs for PICs Read Article

Photonic Chip Drives Scalable Data Transfer Read Article

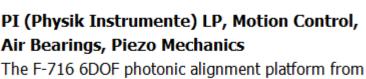
Dutch Foundry SMART Photonics Secures \$111M in Financing Read Article

Photonic Chip Transforms Single Light Beam into Multiple Read Article

**Featured Products & Services** 

Active-passive GaN Scheme Shows Promise for Photonic Integration Read Article

### Photonic Alignment **Platform**

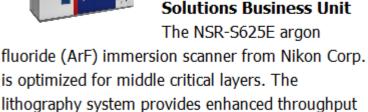


on air bearings for maintenance-free, 24/7 operation in alignment and probing applications.

PI (Physik Instrumente) is a six-axis system based

Visit Website

Request Info



Solutions Business Unit The NSR-S625E argon

Lithography System

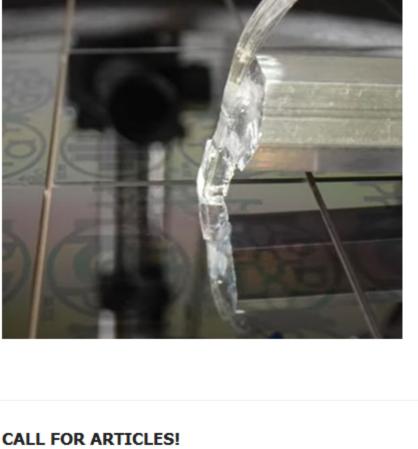
Nikon Corp., Digital

is optimized for middle critical layers. The lithography system provides enhanced throughput

and operational stability for the efficient production of semiconductor devices. Visit Website Request Info

PIC Devices

**Latest Webinars** 



### Production-scale testing of silicon photonic devices continues to be a challenge due to the multi-degree-of freedom, highprecision, optical alignments required for wafer- and die-level testing. Wide variances in chip designs and coupling features

**Precision Automation Principles for** the Optimal Testing and Packaging of

complicate test procedures, making it difficult to identify a system capable of producing repeatable measurements across various topologies. Brett Heintz of Aerotech Inc. provides a guide for selecting precision motion equipment to minimize the impact of positioning errors on optical alignment test results. Presented by Aerotech. Watch Now

Photonics Media is currently seeking technical feature articles on a variety of topics for publication in our magazines (Photonics Spectra, BioPhotonics, and Vision Spectra). Please submit an informal 100-word abstract to editorial@Photonics.com, or use our online submission form.



f 💿 in 😼 🗅

Questions: info@photonics.com Unsubscribe | Subscribe | Subscriptions | Privacy Policy | Terms and Conditions of Use

LAURIN PUBLISHING